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Date December 8, 2005 Time

File no. Our Ref: XA-9512 Pages including
cover

Re: Appln. No.: 09/897,902

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XA-9512
PATENT APPLICATIONRESPONSE UNDER 37 C.F.R. 1.116
EXPEDITED PROCEDURE - EXAMINING GROUP 2112

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

First Named Inventor: HAYASHI, ISAMU Art Unit: 2112

Appln. No.: 09/897,902 Examiner: C.Lee

Filed: July 5, 2001 Conf. No.: 1411

For: MICROPROCESSOR, SEMICONDUCTOR
MODULE AND DATA PROCESSING SYSTEM

* * *

AMENDMENT AFTER FINAL REJECTIONCommissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Final Office Action of September 8, 2005, please amend the above-identified patent application as indicated below.

Amendments to the claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 6 of this paper.